

Patent number: JP2254677 (A)
Publication date: 1990-10-15
Inventor(s): SAKAI KIYOSHI
Applicant(s): NIPPON ELECTRIC CO
Classification:
 - international: G06F3/06; G11B20/10; G06F3/06; G11B20/10; (IPC1-7): G06F3/06; G11B20/10
 - european:
Application number: JP19890073749 19890328
Priority number(s): JP19890073749 19890328

View INPADOC patent family

View list of citing documents

[Report a data error here](#)

Abstract of JP 2254677 (A)

PURPOSE: To speed up an information processing by providing a mode storage part to store a mode to indicate the beacon of the processing as to an error related to a block number detected with a data format.

CONSTITUTION: A data format control circuit 14 arranges the format of write data by adding the block number designated with a microprocessor 15 and, simultaneously, adding an error correcting code, a period signal, etc., at the time of transferring the write data from a data buffer 13 to a magnetic tape device 3, further, detects the error of the block number and corrects it. Namely, a mode storage part 17 stores the mode to indicate whether to execute an abnormality report to a high order device in suspending the processing or to continue the processing in ignoring the error when the microprocessor 15 receives the error related to the block number detected by the data format control circuit 14. By consciously ignoring the error of the block number, the effective utilization of the other data and the speed-up of the information processing can be executed.

